



Attorney Docket No. 0756-1996

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inter-Patent Application of:

Shunpei YAMAZAKI et al.

Serial No. 09/352,362

Filed: July 13, 1999

For: CRYSTALLINE SEMICONDUCTOR

THIN FILM, METHOD OF

FABRICATING THE SAME,

SEMICONDUCTOR DEVICE, AND

METHOD OF FABRICATING THE

SAME

) Group Art Unit: 2815

) Examiner: J. Diaz

) CERTIFICATE OF MAILING

) I hereby certify that this correspondence is
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) 2006.

) Rose Fitch

CORRECTION TO PREVIOUSLY SUBMITTED INFORMATION DISCLOSURE

STATEMENT

Honorable Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

It has come to applicant's attention that several Information Disclosure Statements previously submitted included inadvertent typographical errors or incomplete information.

Specifically, the Information Disclosure Statement filed October 14, 1999 included the following inaccuracy:

1) The article to H. Furue et al. did not include page numbers. The page numbers are "pp. 782-785."

2) The article to S. Inui et al. contained an error. The correct title is: *Thresholdless Antiferroelectricity in Liquid Crystals and its Application to Displays*.

The Information Disclosure Statement filed March 21, 2002 included the following inaccuracies:

1) The article to Aya et al. did not include the volume number. The volume number is "Vol. TFTP1-3."


2) The article to Abe et al. did not include the volume number. The volume number is "Vol. TFTP3-2."

The Information Disclosure Statement filed September 20, 2002 included the following inaccuracy:

1) U.S. Patent 5,620,910 incorrectly listed with the inventor as Termoto. The correct inventor should be Teramoto.

The above inaccuracies are corrected on the 1449 Form attached hereto. This *Correction to Previously Submitted Information Disclosure Statements* is being submitted herewith with the payment of the Issue Fee. The Examiner is requested to initial the attached PTO Form 1449 evidencing consideration of the above references and line through these references on the previously submitted Information Disclosure Statements to ensure that the correct information is printed correctly on any issued patent.

Respectfully submitted,


Eric J. Robinson
Reg. No. 38,285

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PTO/SB/08A (08-00)

Approved for use through 10/31/2002. OMB 0651-0031

U.S. Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

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Substitute for form 1449A/PTO		Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)		Application Number	09/352,362
		Filing Date	July 13, 1999
		First Named Inventor	Shunpei YAMAZAKI et al.
		Group Art Unit	2815
		Examiner Name	J. Diaz
Sheet 1 of 1	Attorney Docket Number	0756-1996	

U.S. PATENT DOCUMENTS						
Examiner Initials	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
		5,620,910		Teramoto	04/15/1997	

FOREIGN PATENT DOCUMENTS								
Examiner Initials	Cite No. ¹	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁴
		Office ³	Number ⁴	Kind Code ⁵ (if known)				

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS			
Examiner Initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		H. Furue et al., <i>Characteristics and Driving Scheme of Polymer-Stabilized Monostable FLC</i> Exhibiting Fast Response Time and High Contrast Ratio with Gray-Scale Capability, SID 1998, pp. 782-785.	
		S. INUI et al., <i>Thresholdless Antiferroelectricity in Liquid Crystals and its Application to Displays</i> , J. Mater. Chem., 6(4), pages 671-673, 1996.	
		AYA et al., <i>Improvement of SPC Poly-Si Film Using the ELA Method</i> , AM-LCD 97, Digest of Technical Papers, Vol. TFTP1-3, Pages 167-170, 1997.	
		ABE et al., <i>High Performance Poly-Crystalline Silicon TFT's Fabricated Using the SPC and ELA Methods</i> , AM-LCD 98, Digest of Technical Papers, Vol. TFTP3-2, Pages 85-88, 1998.	

Examiner Signature	Date Considered
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.